

ABSTRACT OF THE DISCLOSURE

A circuit substrate comprises a first substrate on a first surface of which circuit elements are loaded, a
5 second substrate on which the first substrate is loaded, and noise reduction elements. Each of the noise reduction elements is sandwiched between an area of a second surface of the first substrate over against the first surface of the first substrate and a surface of the second substrate
10 facing the second surface of the first substrate. The noise reduction element is connected between a power source terminal of the second surface of the first substrate and a power source terminal of the surface of the second substrate, and/or between a ground terminal of the second
15 surface of the first substrate and a ground terminal of the surface of the second substrate.